

PRODUCT SPECIFICATIONS

	SLC 5/03		
Catalog Number 1747-	L531	L532	L533
Memory Size (Words)	8K	16K	32K
Backplane (mA) at 5V	500 mA		
Backplane Current (mA) at 24V	175 mA		
Digital I/O, Max.	4096 discrete inputs / 4096 discrete outputs		
Max. Local Chassis / Slots	3 chassis / 30 slots		
On-Board Communications	DH-485 and RS-232		
Optional Memory Module	Flash EPROM		
Programming	RSLogix 500		
Programming Instructions	107		
Typical Scan Time*	1 ms/K		
Program Scan Hold-up Time After Loss of Power	20ms...3 s (dependent on power supply loading)		
Bit Execution (XIC)	0.44 μ s		
Clock / Calendar Accuracy	\pm 54 seconds/month @ +25°C (+77°F), \pm 81 seconds/month @ +60°C (+140°F)		

* The scan times are typical for a 1K ladder logic program consisting of simple ladder logic and communication servicing. Actual scan times depend on your program size, instructions used, and the communication protocol.

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